7. Describe the following:

15

- (i) RISC processors
- (ii) Coprocessors
- (iii) Interfacing of A/D converter with microcontroller.

Roll No. ....

Total Pages: 04

008403

## May 2024

B. Tech. (ECE) (Fourth Semester)
Microprocessors and Microcontrollers (ECC-03)

Time: 3 Hours]

[Maximum Marks: 75

Note: It is compulsory to answer all the questions (1.5 marks each) of Part A in short. Answer any four questions from Part B in detail. Different sub-parts of a question are to be attempted adjacent to each other.

## Part A

- (a) What are the advantages of using the MOVS and CMPS instructions over the MOV and CMP instructions while working with strings?
  - (b) If a data segment begins at address 2400H, what is the address of the last location in the segment?

    1.5
  - (c) What are the features used in mode 2 in 8255?

(d)	What is the difference between 8085 and			
	8086 microprocessors ?			
(e)	What is data transfer scheme and what are			
	its types ?			
(f)	What will be the frequency of the square			
	wave generated by an 8253/8254 timer in			
	mode-3 ?			
(g)	What are the internal devices of a typical			
	DAC ? 1.5			
(h)	List the instructions of 8051 that affect the			
	overflow flag in 8051.			
(i)	What are the two operating modes of 80186?			
	1.5			
(j)	How do ARM microcontrollers differ from			
	PIC ? 1.5			
Part B				
(a)	Explain the addressing modes of 8085			
	microprocessor with examples. 9			
(b)	Draw and explain the flag register structure			
	of the 8086 microprocessor.			

3.	(a)	Draw and explain the operation of 808	86
		microprocess in maximum mode.	9
	(b)	Write an assembly language program using	18
		8085 microprocessor to find the multiby	te
		addition.	6
4.	Inter	rface DAC with 8086/8085 microprocessed	or
	thro	ugh 8255 PPI. Draw the address mapping ar	ıd
	inter	facing mapping. Also write the subroutine	to
	gene	erate an inverse sawtooth waveform.	15
5.	(a)	Draw and explain the block diagram of the	ne
		8051 microcontroller.	8
	(b)	Write assembly language using 805	1
		microcontrollers to arrange ten 8-bit number	rs
		in descending order.	7
6.	(a)	Illustrate the options available with the Time	er
		Mode (TMOD) register of 8051.	7
	(b)	Draw and explain the block diagram of th	e
		2010	8

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