YMCA UNIVERSITY OF SCIENCE AND TECHNOLOGY B.TECH EXAMINATION (UNDER CBS), MAY 2018 ANALOG INTIGRATED CIRCUITS (E305)



Time: 3hrs

NOTE: Attempt all the questions in part 1 with brief answers (word limit 20-40). Attempt any four Question from part 2. The marks are indicated in front of each question.

PART-1(EACH QUESTIOM CARRY 2 MARKS)

Q.No.1.Answer the following:

- I. State the merits of negative feedback in amplifier ?
- II. What do you mean distortion in amplifiers?
- III. Draw only circuit diagram of crystal- oscillator.
- IV. Why a power amplifier is always preceded by a voltage amplifier?.
- V. Indicate an OP-AMP connected as (a) a scale changer (b) a phase shifter.
- VI. The output voltage of a certain OP-AMP circuit change by 25V is 5µs .What is slew rate?.
- VII. What is an oscillator? Give Barkhausen criteria.
- VIII. Draw only circuit diagram of sample and hold circuit.
 - IX. Give any four applications of logarithmic amplifier.
 - X. Give definition of A and C power amplifier.

PART-2

Q.No.2(a) If n amplifier are connected in cascade, each having higher cut-off frequency f _H . The	
overall cut-off frequency of cascaded circuit is f _H [*] . Determine the relation between	
f _H and f _H [*] .	(5)
(b) Explain R-C coupled amplifier. Discuss the frequency response curve and phase-shift cur	ve. (5)
Q.No.3(a) Discuss effect of negative feedback on input impedence and output impedence	(5)
(b) A class- A power amplifier uses a transformer as a coupling device .The transformer has ratio of 10 and the secondary load is 10Ω . If the zero signal collector current is 100 mA , find	a turn the
maximum power output.	(5)
O No.4(a) Draw the circuit of Wein-bridge oscillator and Explain its operation and determine the fre	quency
of oscillation.	(5)
(b) Drive expression for overall efficiency of class B Puss-pull power amplifier & draw its dig.	(5)
O No.5(a) A crystal-oscillator has following parameters ,L=0.33 henry, C=0.065 Pf ,C _m =1.0 pF and R=5.5 k-Ω,	find series
and parallel resonant frequency and find Q-factor of the crystal.	(5)
(b)A 5mV, 1Khz sinusoidal signal is applied to the input of an OP-AMP integrator for which R	$l_1 = 100 \text{K}\Omega$
and $C=1\mu F$ find the output voltage.	(5)
O No E(a) Draw the circuit for antilog amplifier using op-amp and derive the expression for output v	oltage.
Q.NO.O(a)Draw the chedit for antilog and parts of the	(5)
(b) What is the maximum closed-loop voltage gain that can be used when the input signal va	ried by (5)
0.3V III TOHS, IOI all Or Participants and a second s	

Q.No.7(a). Give characteristics of ideal OP- AMP . (b)Write short note on any two.

- (1) AC coupled amplifier.
- (2) IC-555 timer as astable multivibrator.
- (3) Voltage to current convertor